

REMARKS

The office action posits the following analysis:

1. Multiple threads have multiple thread-private storages. That is, multiple threads-private storages are multiple temporary storages for the object.

2. Multiple temporary storages are interpreted as multiple address buffer. For example, if there are two threads, then there would be two address buffer.

While this analysis may be imaginative, there is no basis whatsoever for positing that multiple threads, having their own storage, would necessarily be stored in a two address buffer. They could be stored in a number of buffers, each only having one address. There is no reason whatsoever to presume that in a buffer having two addresses was or would be used under any analysis of the reference.

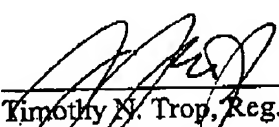
Therefore, reconsideration would be appropriate.

Taken to the logical extreme, if there were five threads, there would be a five address buffer, but no such thing even exists so far as the Applicants' attorney is aware of. Thus, the position is logically and technically untenable.

Therefore, reconsideration is requested.

Respectfully submitted,

Date: July 14, 2006

  
\_\_\_\_\_  
Timothy N. Trop, Reg. No. 28,994  
TROP, PRUNER & HU, P.C.  
1616 South Voss Road, Suite 750  
Houston, TX 77057-2631  
713/468-8880 [Phone]  
713/468-8883 [Fax]

Attorneys for Intel Corporation